INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)	Application Number
BUR920000229US2	10/665 321

Applicant(s)
BUFFET, et al.

Filing Date			Group Art Unit
09	20	03	2829

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
VN		5,757,816	05/26/1998	Al-Assadi et al.			

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation YES	N

OTHER DOCUMENTS   *(Including Author, Title, Date, Pertinent Pages, Etc.)*

VN		"Quiescent Signal Analysis for IC Diagnosis", ITC Workshop, 10/5/-10/6 2000. James F. Plusquellic, Chintan Patel and Ying Ouyang, Department of Computer Science & Electrical Engineering, University of Maryland, Baltimore County.

EXAMINER	DATE CONSIDERED
[signature] Nguyen	05/23/05

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